CRYSTAL OSCILLATOR (Programmable)
SPREAD SPECTRUM
OUTPUT: CMOS

NEW

SG - 9101 series

- Frequency range　　: 0.67 MHz ~ 170 MHz (1 ppm Step)
- Supply voltage　　　: 1.62 V ~ 3.63 V
- Function　　　　　　: Output enable (OE) or Standby (ST)
- Down or Center spread modulation
- Configurable spreading
    3 modulation profile (Hershey-kiss, Sine-wave, Triangle),
    4 modulation frequency, 6 spread percentage
- Package　　　　　　: 2.5 x 2.0, 3.2 x 2.5, 5.0 x 3.2, 7.0 x 5.0 (mm)
- PLL technology to enable short lead time
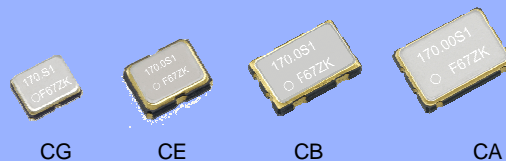- Available field oscillator programmer "SG-Writer II"

Pb Free　　**RoHS Compliant**

Product Number (please contact us)
SG-9101CA: X1G00530xxxxx00
SG-9101CB: X1G00531xxxxx00
SG-9101CE: X1G00532xxxxx00
SG-9101CG: X1G00529xxxxx00

CG　　CE　　CB　　CA

Specifications (characteristics)

Item		Symbol	Specifications				Conditions/Remarks					
Supply voltage		V_{CC}	1.80 V Typ.		2.50 V Typ.	3.30 V Typ.	-					
			1.62 V ~ 1.98 V	1.98 V ~ 2.20 V	2.20 V ~ 2.80 V	2.70 V ~ 3.63 V						
Output frequency range		f_O	0.67 MHz ~ 170 MHz									
Storage temperature		T_stg	-40 ºC ~ +125 ºC				Storage as single product.					
Operating temperature		T_use	-40 ºC ~ +85 ºC									
			-40 ºC ~ +105 ºC									
Current consumption		I_{CC}	3.4 mA Max.	3.5 mA Max.	3.6 mA Max.	3.7 mA Max.	T_use = +105 ºC	No load, f_O = 20 MHz				
			2.9 mA Typ.		3.0 mA Typ.	3.2 mA Typ.	T_use = +25 ºC					
			5.7 mA Max.	6.0 mA Max.	6.9 mA Max.	8.3 mA Max.	T_use = +105 ºC	No load, f_O = 170 MHz				
			4.9 mA Typ.		5.9 mA Typ.	7.0 mA Typ.	T_use = +25 ºC					
Output disable current		I_dis	3.4 mA Max.	3.4 mA Max.	3.5 mA Max.	3.7 mA Max.	OE = GND, f_O = 170 MHz					
Standby current		I_std	0.9 µA Max.	1.0 µA Max.	1.5 µA Max.	2.5 µA Max.	T_use = +105 ºC	ST = GND				
			0.3 µA Typ.	0.4 µA Typ.	0.5 µA Typ.	1.1 µA Typ.	T_use = +25 ºC					
Symmetry		SYM	45 % ~ 55 %				50 % V_{CC} Level					

Output voltage (DC characteristics):

V_{OH} : 90 % V_{CC} Min.

V_{OL} : 10 % V_{CC} Max.

I_{OH}/I_{OL} Conditions						[mA]
Rise/Fall time		Vcc	*A	*B	*C	*D
Default (f_O > 40 MHz), Fast	I_{OH}		-2.5	-3.5	-4.0	-5.0
	I_{OL}		2.5	3.5	4.0	5.0
Default (f_O ≤ 40 MHz)	I_{OH}		-1.5	-2.0	-2.5	-3.0
	I_{OL}		1.5	2.0	2.5	3.0
Slow	I_{OH}		-1.0	-1.5	-2.0	-2.5
	I_{OL}		1.0	1.5	2.0	2.5

*A : 1.62 V ~ 1.98 V, *B : 1.98 V ~ 2.20 V,
*C : 2.20 V ~ 2.80 V, *D : 2.70 V ~ 3.63 V

Item		Symbol	Specifications	Conditions/Remarks
Output load condition		L_CMOS	15 pF Max.	-
Input voltage		V_{IH}	70 % V_{CC} Min.	OE or ST
		V_{IL}	30 % V_{CC} Max.	
Rise and Fall time	Default	tr/tf	3.0 ns Max.	f_O > 40 MHz
			6.0 ns Max.	f_O ≤ 40 MHz
	Fast		3.0 ns Max.	f_O = 0.67 MHz ~ 170 MHz
	Slow		10.0 ns Max.	f_O = 0.67 MHz ~ 20 MHz
Disable Time		t_stp	1 µs Max.	Measured from the time OE or ST pin crosses 30 % V_{CC}
Enable Time		t_sta	1 µs Max.	Measured from the time OE pin crosses 70 % V_{CC}
Resume Time		t_res	3 ms Max.	Measured from the time ST pin crosses 70 % V_{CC}
Start-up time		t_str	3 ms Max.	Measured from the time V_{CC} reaches its rated minimum value, 1.62 V

Rise and Fall time conditions: 20 % - 80 % V_{CC}, L_CMOS = 15 pF

Spread spectrum configuration

④	C: Center spread modulation	⑤Code	02	05	07	10	15	20
		Spread percentage	±0.25 %	±0.5 %	±0.75 %	±1.0 %	±1.5 %	±2.0 %
	D: Down spread modulation	⑤Code	05	10	15	20	30	40
		Spread percentage	-0.5 %	-1.0 %	-1.5 %	-2.0 %	-3.0 %	-4.0 %

Modulation frequency: 25.4 kHz (default), 6.3 kHz, 8.5 kHz, 12.7 kHz　　　Modulation profile: Hershey-kiss (default), Sine-wave, Triangle

Product Name

SG-9101CG 170.000000MHz C 20 P H A A A
①　　②　　　③　　④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩

①Model, ②Package type, ③Frequency,

④Spread type, ⑤Spread percentage code,

⑥Function, ⑦Operating temperature,

⑧Modulation frequency, ⑨Modulation profile, ⑩Rise/Fall time

②Package Type
CA: 7.0 mm x 5.0 mm
CB: 5.0 mm x 3.2 mm
CE: 3.2 mm x 2.5 mm
CG: 2.5 mm x 2.0 mm

④Spread type
C: Center spread
D: Down spread

⑥Function
P: Output enable
S: Standby

⑦Operating temperature
G: -40 ℃ ~ +85 ℃
H: -40 ℃ ~ +105 ℃

⑧Modulation frequency
A: 25.4 kHz (default)
B: 12.7 kHz
C: 8.5 kHz
D: 6.3 kHz

⑨Modulation profile
A: Hershey-kiss (default)
B: Sine-wave
C: Triangle

⑩Rise/Fall time
A: Default
B: Fast
C: Slow

Pin description

Pin	Name	I/O type	Function	
1	OE	Input	Output enable	High: Specified frequency output from OUT pin
				Low: Out pin is low (weak pull down), only output driver is disabled.
	ST	Input	Standby	High: Specified frequency output from OUT pin
				Low: Out pin is low (weak pull down), Device goes to standby mode. Supply current reduces to the least as I_std.
2	GND	Power	Ground	
3	OUT	Output	Clock output	
4	V_{CC}	Power	Power supply	

External dimensions　　　　　　(Unit: mm)

SG-9101CG

SG-9101CE
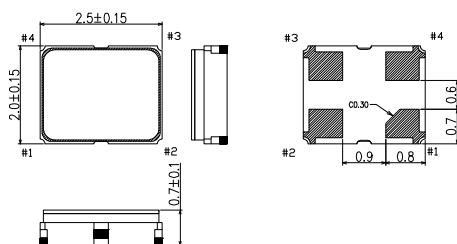
SG-9101CB

SG-9101CA

Footprint (Recommended)　　　(Unit: mm)

SG-9101CG
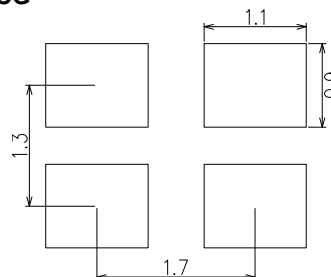
SG-9101CE
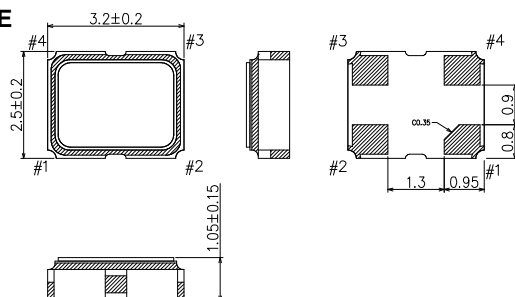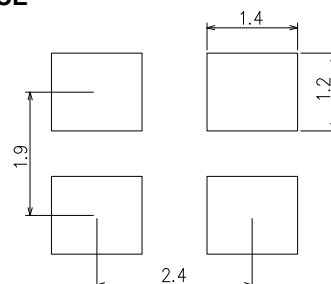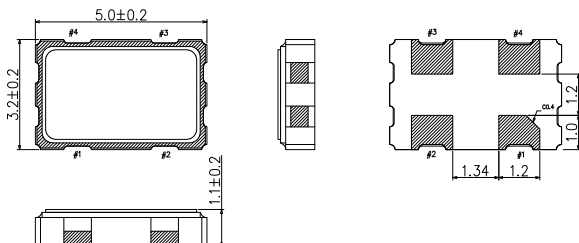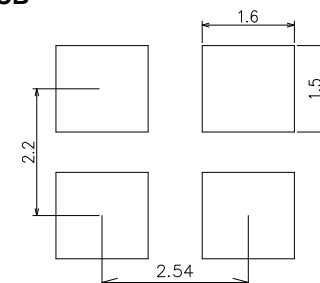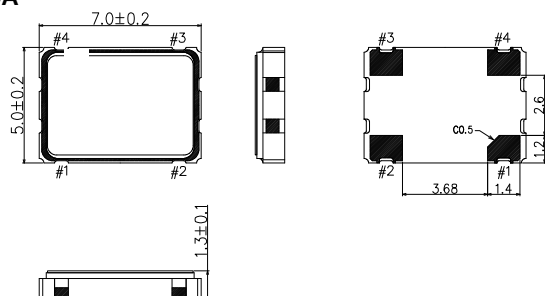
SG-9101CB

SG-9101CA
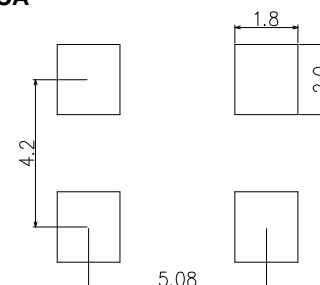
■Notes:

In order to achieve optimum jitter performance, the 0.1 µF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.
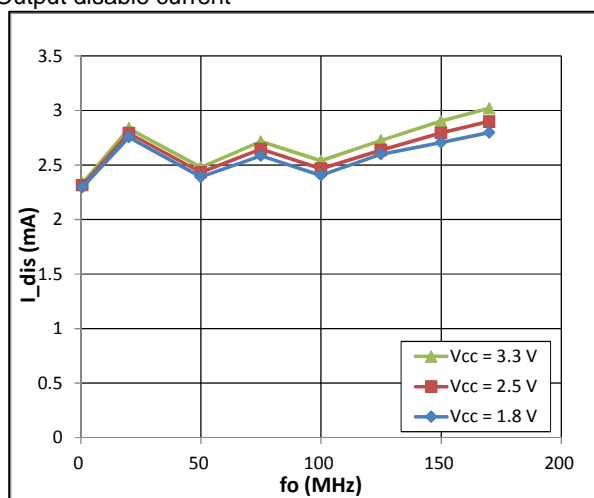
Specification Graph （Typical supplemental specification. Unless otherwise specified T_use = 25 ℃, L_CMOS = 15pF）
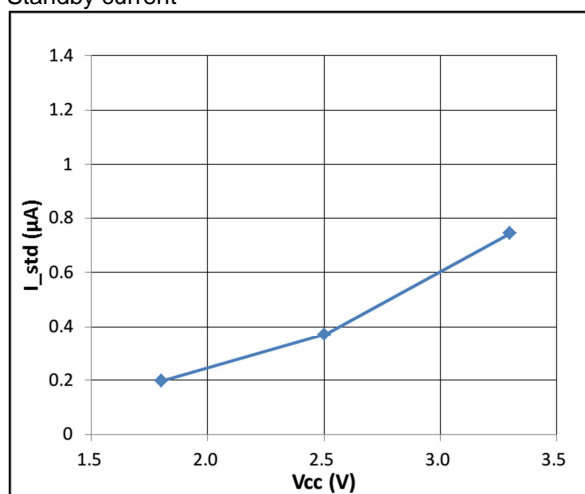
Current Consumption
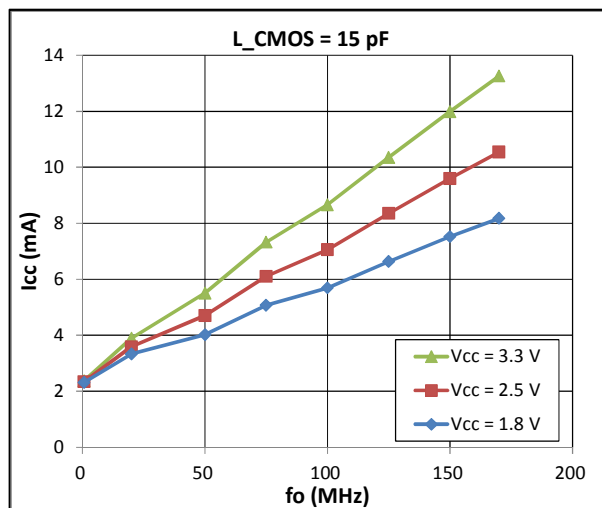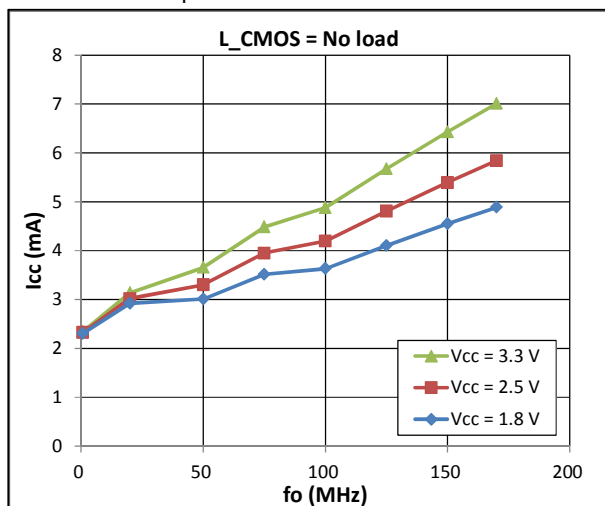
Output disable current

Standby current

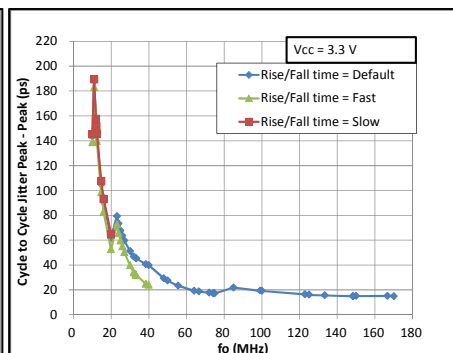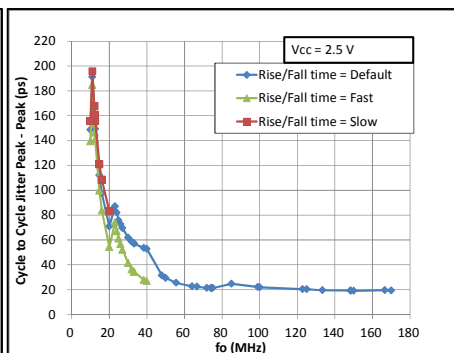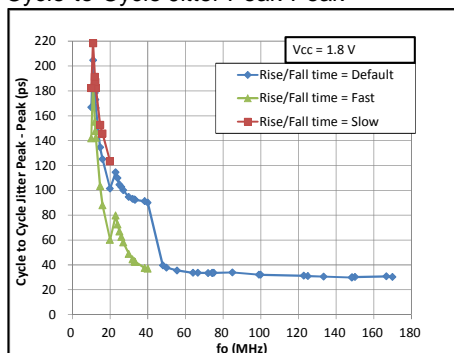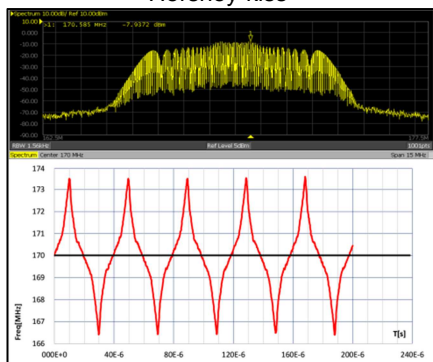
■Notes:
Spead percentage : ±2.0 % , Modulation frequency : 25.4 kHz , Modulation profile : Hershey-kiss

Specification Graph （Typical supplemental specification. Unless otherwise specified T_use = 25 ℃, L_CMOS = 15pF）

Cycle-to-Cycle Jitter Peak-Peak

■Notes:
Spead percentage : ±2.0 % , Modulation frequency : 25.4 kHz , Modulation profile : Hershey-kiss
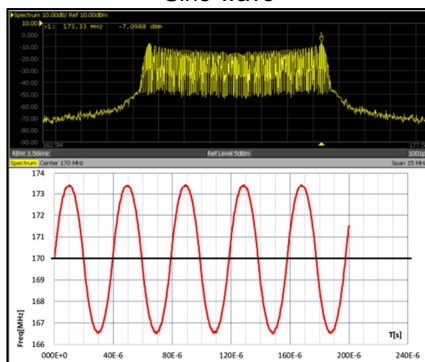
Spread Spectrum Specification Graph

Spread Spectrum Profile   fo : 170 MHz / Spread spectrum : ±2.0 % / Modulation frequency : 25.4 kHz
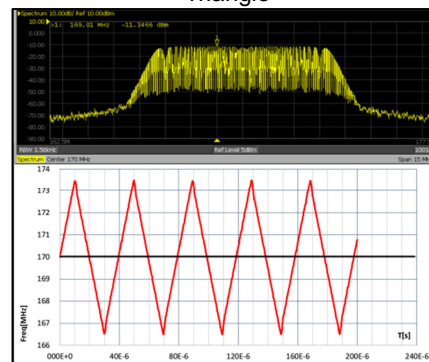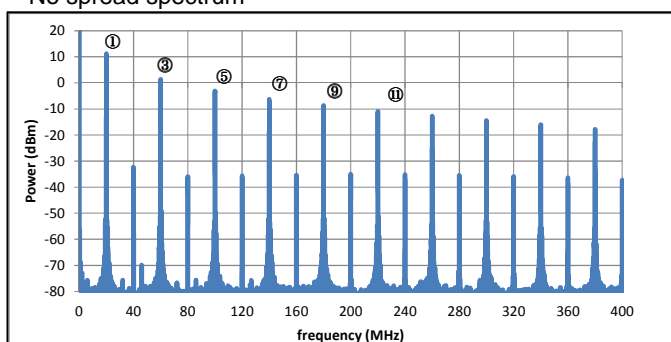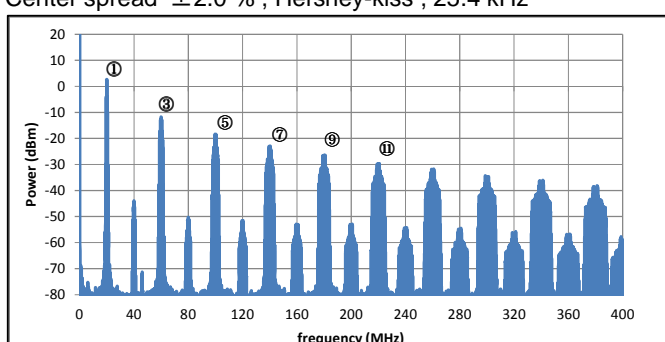
Hershey-kiss

Sine-wave

Triangle

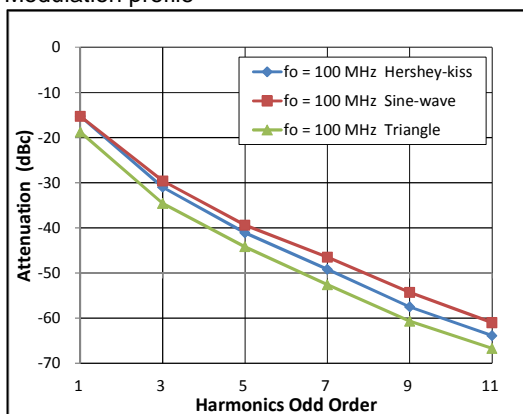## Harmonics Specification Graph
（Typical supplemental specification. Unless otherwise specified T_use = 25 ℃, L_CMOS = 15pF, Vcc = 3.3 V）
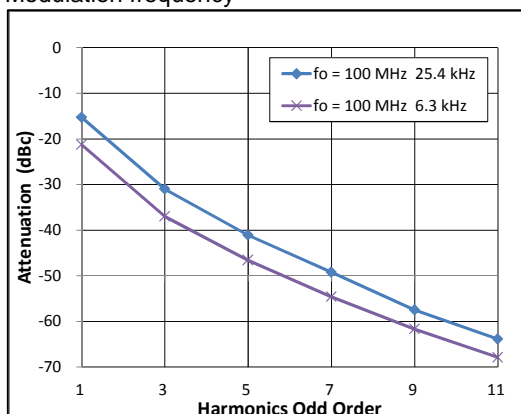
Harmonics spectrum (fo = 20 MHz)

No spread spectrum
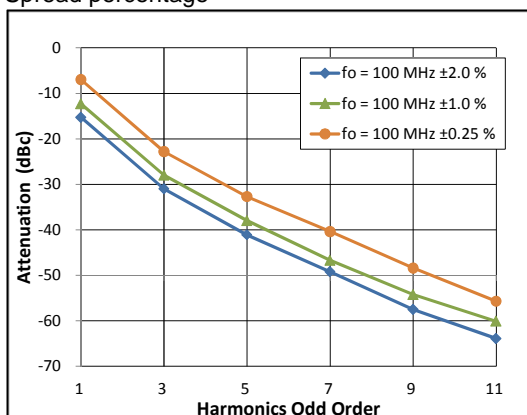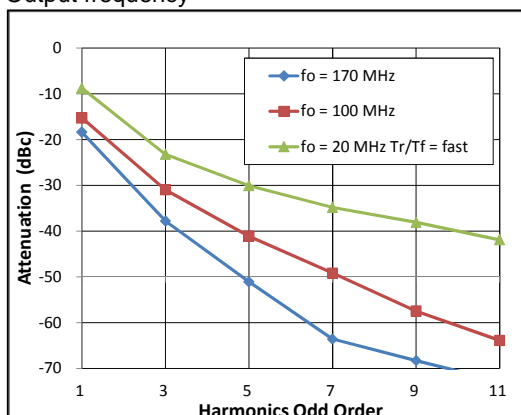
Center spread ±2.0 % , Hershey-kiss , 25.4 kHz

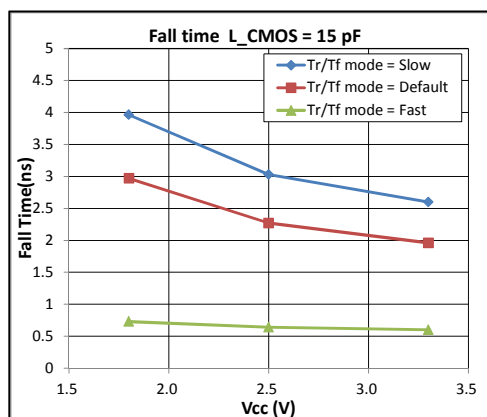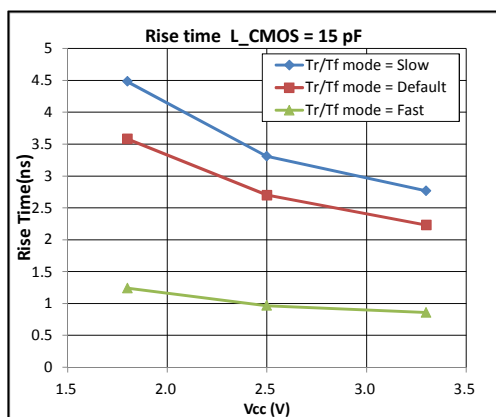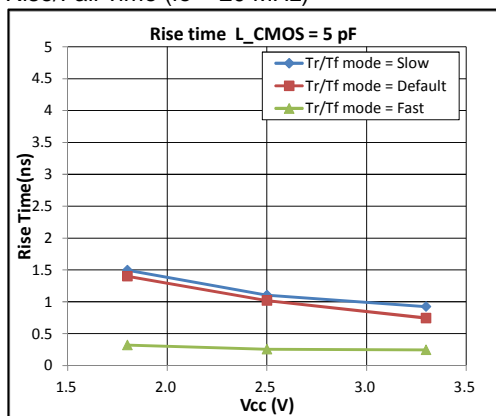Modulation profile

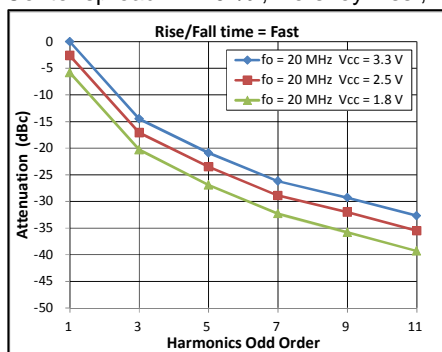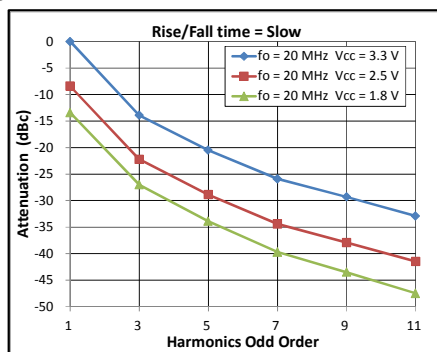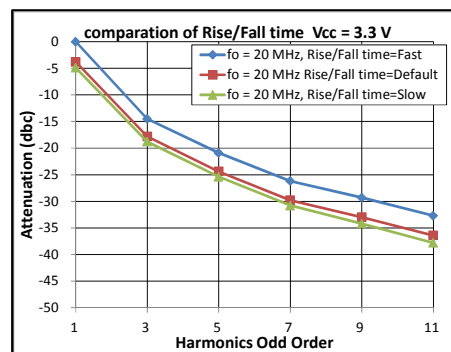Modulation frequency

Spread percentage

Output frequency

■Notes:
    Hermonics order attenuation is normalizing to no-spread spectrum mode.

Specification Graph（Typical supplemental specification. Unless otherwise specified T_use = 25 ℃, L_CMOS = 15pF）

Rise/Fall Time (fo = 20 MHz)

Rise time L_CMOS = 5 pF

- Tr/Tf mode = Slow
- Tr/Tf mode = Default
- Tr/Tf mode = Fast

Rise Time(ns) vs Vcc (V)

Fall time L_CMOS = 5 pF

- Tr/Tf mode = Slow
- Tr/Tf mode = Default
- Tr/Tf mode = Fast

Fall Time(ns) vs Vcc (V)

Rise time L_CMOS = 15 pF

- Tr/Tf mode = Slow
- Tr/Tf mode = Default
- Tr/Tf mode = Fast

Rise Time(ns) vs Vcc (V)

Fall time L_CMOS = 15 pF

- Tr/Tf mode = Slow
- Tr/Tf mode = Default
- Tr/Tf mode = Fast

Fall Time(ns) vs Vcc (V)
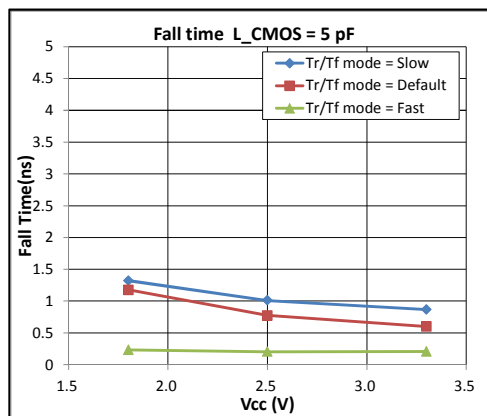
Harmonics comparison
Center spread ±2.0 % , Hershey-kiss , 25.4 kHz

Rise/Fall time = Fast

- fo = 20 MHz Vcc = 3.3 V
- fo = 20 MHz Vcc = 2.5 V
- fo = 20 MHz Vcc = 1.8 V

Attenuation (dBc) vs Harmonics Odd Order

Normalize to Vcc = 3.3V.

Rise/Fall time = Slow

- fo = 20 MHz Vcc = 3.3 V
- fo = 20 MHz Vcc = 2.5 V
- fo = 20 MHz Vcc = 1.8 V

Attenuation (dBc) vs Harmonics Odd Order

Normalize to Vcc = 3.3V.

comparation of Rise/Fall time Vcc = 3.3 V

- fo = 20 MHz, Rise/Fall time=Fast
- fo = 20 MHz Rise/Fall time=Default
- fo = 20 MHz, Rise/Fall time=Slow

Attenuation (dbc) vs Harmonics Odd Order

Normalize to Rise/Fall time = "Fast".

■Notes:

frequency	slow	default	fast
0.67 M – 20 M	See Slow	See Default	See Fast
20 M – 40 M	-	See Default	See Fast
40 M – 170 M	-	See Fast	See Fast

Simulation Model

・ IBIS Model is available upon request. Please contact us.
Information Required: Oscillator operating condition (i.e. Power Supply, Rise/Fall Time, Temperature)
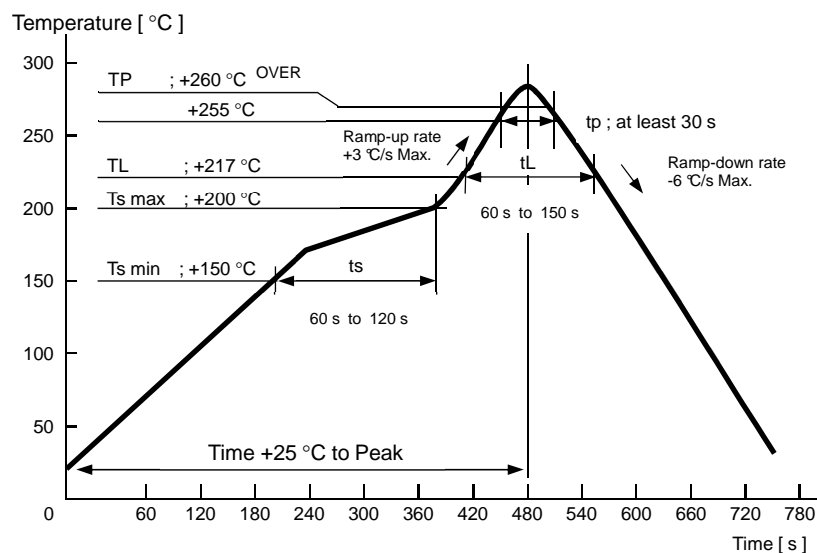
ESD Rating

Test items	Breakdown voltage
Human Body Model (HBM)	2000V
Machine Model (MM)	250V
Charged Device Model (CDM)	750V

Device Material & Environmental Information

Model	Package Dimensions	# of Pins	Reference Weight (Typ.)	Terminal Material	Terminal Plating	Complies With EU RoHS	Pb Free	MSL Rating	Peak Temp. (Max)
SG-9101CG	2.5x2.0x0.7mm	4	13 mg	W	Au	Yes	Yes	1	260°C
SG-9101CE	3.2x2.5x1.0mm	4	25 mg	W	Au	Yes	Yes	1	260°C
SG-9101CB	5.0x3.2x1.1mm	4	51 mg	W	Au	Yes	Yes	1	260°C
SG-9101CA	7.0x5.0x1.3mm	4	143 mg	W	Au	Yes	Yes	1	260°C

SMD products Reflow profile(example)
The availability of the heat resistance for reflow conditions of JEDEC-STD-020D.01 is judged individually. Please inquire.

Temperature [°C]

TP ; +260 °C OVER
+255 °C
tp ; at least 30 s
Ramp-up rate +3 °C/s Max.
TL ; +217 °C
tL
Ramp-down rate -6 °C/s Max.
Ts max ; +200 °C
60 s to 150 s
Ts min ; +150 °C
ts
60 s to 120 s
Time +25 °C to Peak

Time [s]

|		
Pb Free	●	Pb free.
RoHS Compliant	●	Complies with EU RoHS directive. ➢ About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive. (Contains Pb in sealing glass, high melting temperature type solder or other.)

Device Marking

Model	Factory Programmed Part Marking	Field Programmable Part Marking (Blank Samples)
SG-9101CG	Frequency 170.S1 Product code ○A23DK 1pin mark Lot No.	Product code S1 ○A23DK 1pin mark Lot No.
SG-9101CE	Frequency 170.0S1 Product code ○ A23DK 1pin mark Lot No.	Product code S1 ○ A23DK 1pin mark Lot No.
SG-9101CB	Frequency 170.0S1 Product code ○ A23DK 1pin mark Lot No.	Product code S1 ○ A23DK 1pin mark Lot No.
SG-9101CA	Frequency 170.00S1 Product code ○ A23DK 1pin mark Lot No.	Product code S1 ○ A23DK 1pin mark Lot No.

Standard Packing Specification

SMD products are packed in the shipping carton as below table in accordance with taping standards EIA-481 and IEC-60286
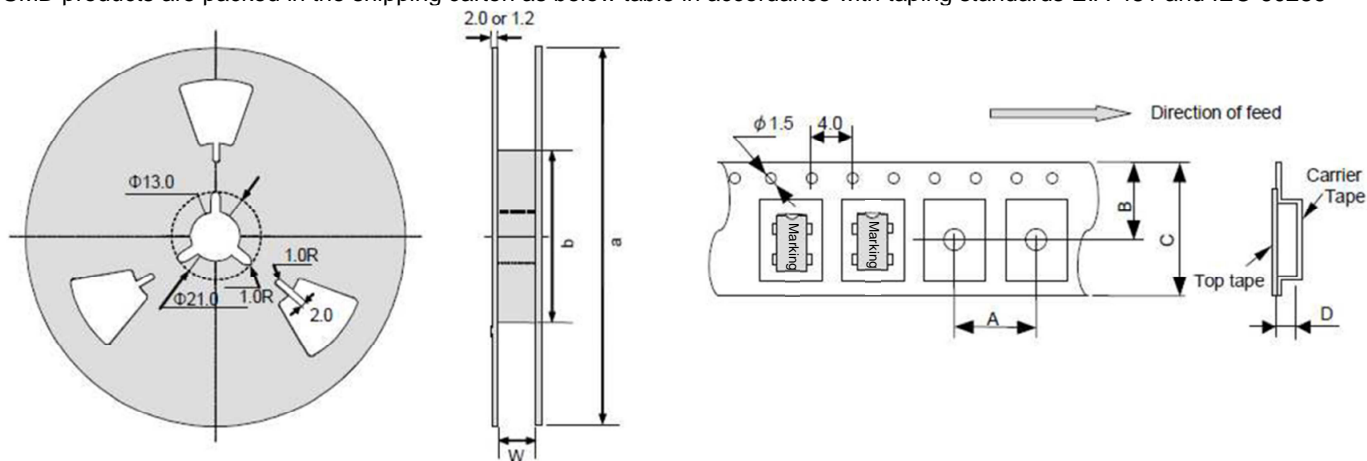
Standard Packing Quantity & Dimension(Unit mm)

Model	Quantity (pcs/Reel)	Reel Dimension			Career Tape Dimension				Direction of Feed (L= Left Direction)
		a	b	W	A	B	C	D	
SG-9101CG	3000	Φ180	Φ60	9	4	5.25	8	1.15	L
SG-9101CE	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
SG-9101CB	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
SG-9101CA	1000	Φ254	Φ100	17.5	8	9.25	16	2.3	L

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM
CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs,

Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired ISO/TS 16949 certification that is requested strongly by major automotive manufacturers as standard.

ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

►**Explanation of the mark that are using it for the catalog**

Pb Free	►Pb free.
RoHS Compliant	►Complies with EU RoHS directive. *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive. (Contains Pb in sealing glass, high melting temperature type solder or other.)
For Automotive	►Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.
Automotive Safety	►Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

Notice